2SC5592

Silicon NPN epitaxial planer type

For DC-DC converter

For various driver circuits

■ Features

- \bullet Low collector to emitter saturation voltage $V_{\text{CE}(\text{sat})}$, large current capacitance
- High-speed switching
- Mini type 3-pin package, allowing downsizing and thinning of the equipment.
- Complementary pair with 2SA2010

■ Absolute Maximum Ratings $T_a = 25$ °C

Parameter	Symbol	Rating	Unit
Collector to base voltage	V _{CBO}	15	V
Collector to emitter voltage	V _{CEO}	15	V
Emitter to base voltage	V _{EBO}	5	V
Peak collector current	I _{CP}	10	A
Collector current	I_{C}	2.5	A
Collector power dissipation *	$P_{\rm C}$	600	mW
Junction temperature	Tj	150	°C
Storage temperature	T_{stg}	-55 to +150	°C

Note) *: Measure on the ceramic substrate at $15\times15\times0.6~mm^3.$

Marking Symbol: 2T

■ Electrical Characteristics $T_a = 25$ °C ± 3 °C

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector cutoff current	I_{CBO}	$V_{CB} = 10 \text{ V}, I_E = 0$			0.1	μΑ
Collector to base voltage	V_{CBO}	$I_C = 10 \mu\text{A}, I_E = 0$	15			V
Collector to emitter voltage	V _{CEO}	$I_{\rm C} = 1 \text{ mA}, I_{\rm B} = 0$	15			V
Emitter to base voltage	V _{EBO}	$I_E = 10 \ \mu A, \ I_C = 0$	5			V
Forward current transfer ratio *1	h _{FE1}	$V_{CE} = 2 \text{ V}, I_{C} = 100 \text{ mA}$	400		1 000	
	h _{FE2}	$V_{CE} = 2 \text{ V}, I_{C} = 2.5 \text{ A}$	280			
Collector to emitter saturation voltage *1	V _{CE(sat)}	$I_C = 1 \text{ A}, I_B = 10 \text{ mA}$		110		mV
		$I_C = 2.5 \text{ A}, I_B = 50 \text{ mA}$		220	320	mV
Collector output capacitance	C _{ob}	$V_{CB} = 10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$		30		pF
Transition frequency	f_{T}	$V_{CB} = 10 \text{ V}, I_{E} = -50 \text{ mA}$ f = 200 MHz		180		MHz
Turn-on time *2	t _{on}			30		ns
Storage time *2	t _{stg}			100		ns
Fall time *2	$t_{\rm f}$			10		ns

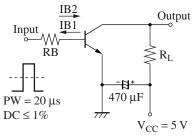
Note) *1: Rank classification (≤ 1 ms)

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^{*2:} Refere to the measurement circuit.

2SC5592 Transistors

■ Measurement Circuit



-201B1 = 201B2 = IC = 1.5 A

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